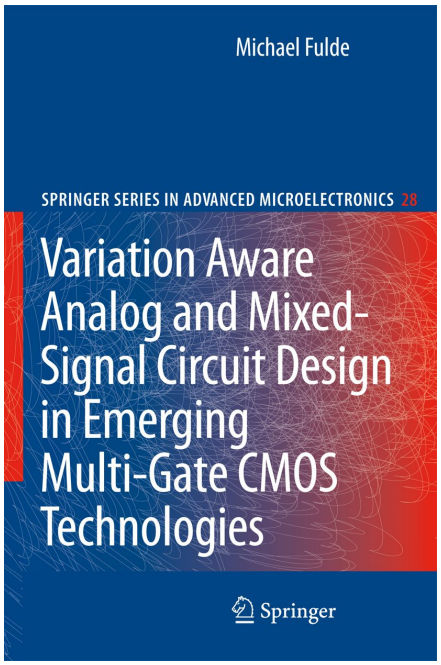


**Springer**

1st edition

2010, X, 127 p.

**Printed book**
Hardcover

**Printed book**
Hardcover
ISBN 978-90-481-3279-9
$ 169,99
Available

**Discount group**
Professional Books (2)

**Product category**
Monograph

**Series**
Springer Series in Advanced Microelectronics

**Other renditions**
Softcover
ISBN 978-94-007-3083-0

Engineering : Circuits and Systems

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# Variation Aware Analog and Mixed-Signal Circuit Design in Emerging Multi-Gate CMOS Technologies

- First book that covers high-k related design aspects regarding analog/mixed-signal circuits
- First hardware based analog/mixed-signal circuit assessment in multi-gate CMOS
- Close link to device and technology
- Covers tunnel devices as outlook beyond CMOS

Since scaling of CMOS is reaching the nanometer area serious limitations enforce the introduction of novel materials, device architectures and device concepts. Multi-gate devices employing high-k gate dielectrics are considered as promising solution overcoming these scaling limitations of conventional planar bulk CMOS. Variation Aware Analog and Mixed-Signal Circuit Design in Emerging Multi-Gate CMOS Technologies provides a technology oriented assessment of analog and mixed-signal circuits in emerging high-k and multi-gate CMOS technologies.





ISBN 978-90-481-3279-9 / BIC: TJFC / SPRINGER NATURE: SCT24068