



Manar El-Chammas, Boris Murmann

Background Calibration of Time-Interleaved Data Converters

Series: Analog Circuits and Signal Processing

- Discusses time-interleaved ADC theory in detail Presents and analyzes a full solution for timing-skew in time-interleaved ADCs Demonstrates how to optimize the design of a time-interleaved ADC for extremely low-power and very high sample rate

This book describes techniques for time-interleaving a number of analog-to-digital data converters to achieve demanding bandwidth requirements. Readers will benefit from the presentation of a low-power solution that can be used in actual products, while alleviating the time-varying signal artifacts that typically arise when implementing such a system architecture.

2012, XX, 124 p.

Printed book

Hardcover

129,99 € | £109.99 | \$159.99

^[1]139,09 € (D) | 142,99 € (A) | CHF

143,00

Softcover

112,14 € | £89.99 | \$139.99

^[1]119,99 € (D) | 123,35 € (A) | CHF

123,50

eBook

95,19 € | £71.50 | \$109.00

^[2]95,19 € (D) | 95,19 € (A) | CHF

106,00

Available from your library or
springer.com/shop

MyCopy ^[3]

Printed eBook for just

€ | \$ 24.99

springer.com/mycopy

Order online at springer.com / or for the Americas call (toll free) 1-800-SPRINGER / or email us at: customerservice@springernature.com. / For outside the Americas call +49 (0) 6221-345-4301 / or email us at: customerservice@springernature.com.

The first € price and the £ and \$ price are net prices, subject to local VAT. Prices indicated with [1] include VAT for books; the €(D) includes 7% for Germany, the €(A) includes 10% for Austria. Prices indicated with [2] include VAT for electronic products; 19% for Germany, 20% for Austria. All prices exclusive of carriage charges. Prices and other details are subject to change without notice. All errors and omissions excepted. [3] No discount for MyCopy.

