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Engineering : Circuits and Systems

El-Chammas, Manar, Murmann, Boris, Texas Instruments, Inc., Dallas, TX, USA

Background Calibration of Time-Interleaved Data Converters

- Discusses time-interleaved ADC theory in detail Presents and analyzes a full solution for timing-skew in time-interleaved ADCs Demonstrates how to optimize the design of a time-interleaved ADC for extremely low-power and very high sample rate

This book describes techniques for time-interleaving a number of analog-to-digital data converters to achieve demanding bandwidth requirements. Readers will benefit from the presentation of a low-power solution that can be used in actual products, while alleviating the time-varying signal artifacts that typically arise when implementing such a system architecture.

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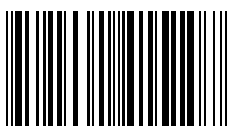
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