

Springer

1st  
edition

2015, XXIII, 219 p. 131 illus.

**Printed book**

Hardcover

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Hardcover

ISBN 978-3-319-18104-2

\$ 99,99

Available

**Discount group**

Professional Books (2)

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Graduate/advanced undergraduate textbook

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**Engineering : Circuits and Systems**

Muthuswamy, Bharathwaj, Banerjee, Santo, Software Engineering, Berkeley, CA, USA

# A Route to Chaos Using FPGAs

**Volume I: Experimental Observations**

- **Course-tested introduction to this field**
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The purpose of this introductory book is to couple the teaching of chaotic circuit and systems theory with the use of field programmable gate arrays (FPGAs). As such, it differs from other texts on chaos: first, it puts emphasis on combining theoretical methods, simulation tools and physical realization to help the reader gain an intuitive understanding of the properties of chaotic systems. Second, the "medium" used for physical realization is the FPGA. These devices are massively parallel architectures that can be configured to realize a variety of logic functions. Hence, FPGAs can be configured to emulate systems of differential equations. Nevertheless maximizing the capabilities of an FPGA requires the user to understand the underlying hardware and also FPGA design software. This is achieved by the third distinctive feature of this book: a lab component in each chapter. Here, readers are asked to experiment with computer simulations and FPGA designs, to further their understanding of concepts covered in the book. This text is intended for graduate students in science and engineering interested in exploring implementation of nonlinear dynamical (chaotic) systems on FPGAs.

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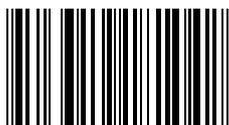
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