Journal of Hardware and Systems Security
Main editors: S. Bhunia; M.M. Tehranipoor

► Highlights new and ground-breaking developments in the field of electronic hardware and systems security
► Covers the interaction of hardware and systems, and between hardware and firmware/software, in the context of security and trust
► Encompasses security of processor, System-on-Chip (SoC), Field Programmable Gate Arrays (FPGA), analog/mixed-signal electronics, all other electronic components, and Printed Circuit Board (PCB)
► Covers all attack vectors, including all forms of side-channel attacks, piracy, reverse engineering, tampering, and hardware Trojan attacks; as well as, all forms of countermeasures at different stages of system design - i.e., architecture definition, design, validation, and deployment
► Publishes archival research results focusing on the security of hardware and system security at all levels of abstraction and the supply chain including track and trace technologies across a variety of industries

This journal examines the rapidly expanding field of electronic hardware and systems security, covering embedded systems, automotive systems, cyber-physical systems, The Internet of Things (IoT), reconfigurable systems, biomedical systems including implants and wearable devices and much more.

On the homepage of Journal of Hardware and Systems Security at springer.com you can
► Sign up for our Table of Contents Alerts
► Get to know the complete Editorial Board
► Find submission information