Next year 2018 is the 250 anniversary of the birth of J.B. Fourier. Even after 250 years, the Fourier transform is used in a large number of engineering applications. To celebrate this special occasion, we organize this special issue on fast Fourier transform hardware implementations.

**Topics**

The topics of this special issue include, but are not limited to:

- FFT hardware architectures and implementations.
- Implementation of rotators (such as CORDIC).
- Study of FFT algorithms.
- Bit reversal circuits.
- Novel applications that use a hardware FFT.

**Submission Guidelines**

Due to this special occasion, we are interested in high-quality, original work that has neither appeared in, nor is under consideration by, other journals or conferences. All submissions will be peer reviewed subject to the standards of the journal. All submissions are expected to provide theoretical/technical contributions that improve the state-of-the-art.

Please submit regular full papers, including all figures, tables, and references, from the JSPS paper submission website http://www.editorialmanager.com/vlsi/default.asp. At the Editorial Manager submission system, select the article type "Special issue on fast Fourier transform (FFT) hardware implementations" for submission to this special issue. Authors are requested to follow the paper format specified in the above journal submission website.

**Important Dates**

- Manuscript submission deadline: December 1, 2017.
- First round review results: February 1, 2018.
- Submission deadline for revisions: March 15, 2018.
- Second round review results: April 15, 2018.
- Final manuscript due for accepted papers: May 15, 2018.
- Publication of the Special Issue: Last months of 2018.

**Guest Editors**

- Dr. Mario Garrido: Associate Professor, Linköping University, Linköping, Sweden.
- Prof. Sau-Gee Chen: Professor, National Chiao Tung University, Hsinchu 300, Taiwan.
- Prof. María-Luisa López-Vallejo: Professor, Technical University of Madrid, Madrid, Spain.