Preface

Manufacturing plays a major role in the development of the country. However, the manufacturing industry is facing several challenges such as rapid product development, flexibility, and low to medium volume, transportation, and low cost etc. Many advanced and unconventional technologies, tools, and software are being developed worldwide to enable solutions to these challenges. Manufacturing and design of integrated circuits (ICs) comprises a large portion of research advances in manufacturing because of the need for precise work. To enable exchange of ideas on current trends in this area, the 1st International Conference on Nano-electronics, Circuits & Communication Systems (NCCS-2015) was organized by the Indian Society of VLSI Education (ISVE), Ranchi and the Institution of Electronics and Telecommunication Engineers (IETE) Ranchi at Advanced Regional Telecom Training Centre (ARTTC) near Jumar River Hazaribag Road Ranchi from 9 to 10th May, 2015. Around 300 papers were received in total, the best of which are presented in this volume. For this selection, all papers were blind reviewed by three expert reviewers and the presentation sessions were reviewed by a six-member expert committee.

There are a total of 35 papers in this volume that cover the core themes of the conference, i.e., design, simulation, verification, implementation and applications of nano-electronics, circuits and communication systems. The paper by L. Rajesh et al. describe user demand wireless network selection using game theory, with this concept unwanted power radiation will be reduced. Ankur Saxena et al. demonstrate his work on review on band notching techniques for ultra-wideband antenna. Biswajit Ghosh et al. demonstrate his research work on comparing energy efficiency of DF relay assisted cooperative and non-cooperative short-range wireless systems. Abhishek Rawat et al. describe dual band octagonal shape microstrip patch antenna at 5.70 and 8.0 GHz. Nabajyoti Mazumdar et al. explain distributed fault tolerant multi-objective clustering algorithm for wireless sensor network. Abhishek Nag et al. describe an autonomous power in clock getting technology in SRAM-based
Authors and editors have taken utmost care in preparing this volume for publication, but there some errors that might have crept in despite our best efforts. Readers are requested to provide their valuable feedbacks on the quality of presentation and alert us of any inadvertent errors or omissions. We expect that the book will be welcomed by students as well as practicing engineers, researchers, and professors.

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