

Preface

RFIDsec 2013, the 9th workshop on RFID Security and Privacy, was held in Graz, Austria, during July 9–11, 2013. More than 50 participants from 13 countries attended the workshop. The Program Committee consisting of 24 experts in the field selected 11 papers out of 23 submissions for publication in the workshop proceedings. Each paper was reviewed by at least three and on average four reviewers.

The program included three invited talks given by international experts in the field. The first invited talk entitled “RFID Privacy: From Transportation Payments to Implantable Medical Devices” was given by Wayne Burleson, who introduced the audience to state of the art RFID privacy issues. The second talk was given by Günther Lackner and Karin Greimel, who talked about “20 Years of MIFARE. From CRYPTO1 to Formal Verification.” They gave an overview of various industry perspectives in the case of the Mifare product family that celebrates its 20 years anniversary. The third talk was given by Lejla Batina entitled “How Light Is Lightweight Crypto”. Her talk focused on RFID identification protocols and hardware-implementation requirements for RFID.

Since 2011, RFIDsec has additionally provided area-relevant tutorials for attendees from academia and industry. In this year’s edition, we offered three tutorials that preceded the workshop. The specific topics were “RFID Introduction and the IAIK DemoTag: A Programmable RFID-Tag Emulator” given by Thomas Korak, Raphael Spreitzer, and Hannes Gross, “Side-Channel Attacks and Fault Analysis” given by Johann Heyszl and Thomas Korak, and “Cryptographic Hardware Design and Performance Metrics” given by Frank K. Gürkaynak.

This year’s edition of RFIDsec was the first in cooperation with the International Association for Cryptologic Research. We would like to thank Bart Preneel for promoting and supporting us to get the IACR “In Cooperation” status. Furthermore, we would like to thank the sponsorships from the Styrian Business Promotion Agency (SFG), the City of Graz, the State of Styria, and NXP Semiconductors. With their support it was possible to provide student stipends for attending the workshop.

Finally, we would like to thank all authors for their paper submission, the Program Committee and all external reviewers for their support in the review process allowing us to accept a number of high-quality papers, and all the attendees of the workshop for their active participation and contribution to this research area.

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Michael Hutter
Jörn-Marc Schmidt



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