# Table of Contents

## Invited Talk

The New Economics of Embedded Systems .......................... Michael Uhler 1

## Application Specific (Co)Design

A Framework for Architectural Description of Embedded System .......... Daniela Cristina Cascini Peixoto and Diógenes Cecílio da Silva Júnior 2

Automatically Customising VLIW Architectures with Coarse Grained Application-Specific Functional Units ............. Diviya Jain, Anshul Kumar, Laura Pozzi, and Paolo Ienne 17

ASIP Architecture Exploration for Efficient Ipsec Encryption: A Case Study .......................................................... Hanno Scharwaechter, David Kammler, AndreasWieferink, Manuel Hohenauer, Kingshuk Karuri, Jianjiang Ceng, Rainer Leupers, Gerd Ascheid, and Heinrich Meyr 33

## System and Application Synthesis

Compact Procedural Implementation in DSP Software Synthesis Through Recursive Graph Decomposition .................... Ming-Yung Ko, Praveen K. Murthy, and Shuvra S. Bhattacharyya 47

An Integer Linear Programming Approach to Classify the Communication in Process Networks ......................... Alexandru Turjan, Bart Kienhuis, and Ed Deprettere 62

Predictable Embedded Multiprocessor System Design .................. Marco Bekooij, Orlando Moreira, Peter Poplavko, Bart Mesman, Milan Pastrnak, and Jef van Meerbergen 77

## Data Flow Analysis

Suppression of Redundant Operations in Reverse Compiled Code Using Global Dataflow Analysis .............................. Adrian Johnstone and Elizabeth Scott 92

Fast Points-to Analysis for Languages with Structured Types .......... Michael Jung and Sorin Alexander Huss 107
Data Partitioning

An Automated C++ Code and Data Partitioning Framework for Data Management of Data-Intensive Applications .......................... 122
Athanasios Milidonis, Grigoris Dimitroulakos, Michalis D. Galanis, George Theodoridis, Costas Goutis, and Francky Catthoor

Combined Data Partitioning and Loop Nest Splitting for Energy Consumption Minimization .......................... 137
Heiko Falk and Manish Verma

On the Phase Coupling Problem Between Data Memory Layout Generation and Address Pointer Assignment .......................... 152
Bernhard Wess and Thomas Zeitlhofer

Task Scheduling

Dynamic Mapping and Ordering Tasks of Embedded Real-Time Systems on Multiprocessor Platforms .......................... 167
Peng Yang and Francky Catthoor

Integrated Intra- and Inter-task Cache Analysis for Preemptive Multi-tasking Real-Time Systems .......................... 182
Yudong Tan and Vincent Mooney

A Fuzzy Adaptive Algorithm for Fine Grained Cache Paging .......................... 200
Susmit Bagchi and Mads Nygaard

Code Generation

DSP Code Generation with Optimized Data Word-Length Selection ........ 214
Daniel Menard and Olivier Sentieys

Instruction Selection for Compilers that Target Architectures with Echo Instructions .......................... 229
Philip Brisk, Ani Nahapetian, and Majid Sarrafsadeh

A Flexible Tradeoff Between Code Size and WCET Using a Dual Instruction Set Processor .......................... 244
Sheayun Lee, Jaejin Lee, Chang Yun Park, and Sang Lyul Min

Author Index .......................... 259
Software and Compilers for Embedded Systems
8th International Workshop, SCOPES 2004, Amsterdam,
The Netherlands, September 2-3, 2004, Proceedings
Schepers, H. (Ed.)
2004, X, 266 p., Softcover
ISBN: 978-3-540-23035-9