Contents

1 Dyninst and MRNet: Foundational Infrastructure for Parallel Tools ........................................... 1
   William R. Williams, Xiaozhu Meng, Benjamin Welton and Barton P. Miller

2 Validation of Hardware Events for Successful Performance Pattern Identification in High Performance Computing .......... 17
   Thomas Röhl, Jan Eitzinger, Georg Hager and Gerhard Wellein

3 Performance Optimization for the Trinity RNA-Seq Assembler ............................................ 29
   Michael Wagner, Ben Fulton and Robert Henschel

4 Power Management and Event Verification in PAPI ................................................... 41
   Heike Jagode, Asim YarKhan, Anthony Danalis and Jack Dongarra

5 Gleaming the Cube: Online Performance Analysis and Visualization Using MALP .......................... 53
   Jean-Baptiste Besnard, Allen D. Malony, Sameer Shende, Marc Pérache and Julien Jaeger

6 Evaluation of Tool Interface Standards for Performance Analysis of OpenACC and OpenMP Programs .................... 67
   Robert Dietrich, Ronny Tschüter, Tim Cramer, Guido Juckeland and Andreas Knüpfert

7 Extending MUST to Check Hybrid-Parallel Programs for Correctness Using the OpenMP Tools Interface ................. 85
   Tim Cramer, Felix Münchhafen, Christian Terboven, Tobias Hilbrich and Matthias S. Müller

8 Event Flow Graphs for MPI Performance Monitoring and Analysis ............................................. 103
   Xavier Aguilar, Karl Fürlinger and Erwin Laure
9 Aura: A Flexible Dataflow Engine for Scalable Data Processing ........................................ 117
   Tobias Herb, Lauritz Thamsen, Thomas Renner and Odej Kao

10 Parallel Code Analysis in HPC User Support ................ 127
   Rene Sitt, Alexandra Feith and Dörte C. Sternel

11 PARCOACH Extension for Hybrid Applications with Interprocedural Analysis ........................... 135
   Emmanuelle Saillard, Hugo Brunie, Patrick Carribault and Denis Barthou

12 Enabling Model-Centric Debugging for Task-Based Programming Models—A Tasking Control Interface ........... 147
   Mathias Nachtmann and José Gracia

13 Evaluating Out-of-Order Engine Limitations Using Uop Flow Simulation .................................. 161
   Vincent Palomares, David C. Wong, David J. Kuck and William Jalby