## Contents

### Modeling and Specification

**Genericity in PAR Platform** ......................................................... 3  
*Jinyun Xue*

**Modeling and Verification of an Interrupt System in μC/OS-III with TMSVL** ................................................................. 15  
*Jin Cui, Zhenhua Duan, Cong Tian, and Nan Zhang*

**The Interchange Format of Tabular Expressions Using XML** .................. 29  
*Mao Huang, Yihai Chen, Ridha Khedri, and Huaikou Miao*

**A GUI-Aided Approach to Formal Specification Construction** .............. 44  
*Shaoying Liu*

### Testing and Debugging

**Automatic Generation of Specification-Based Test Cases by Applying Genetic Algorithms in Reinforcement Learning** ..................... 59  
*Yuji Sato and Taku Sugihara*

**Fault Localization of Timed Automata Using Maximum Satisfiability** ........ 72  
*Shin Nakajima and Si-Mohamed Lamraoui*

**Automated Program Debugging for Multiple Bugs Based on Semantic Analysis** ................................................................. 86  
*Aishan Liu, Li Li, and Jie Luo*

### Model Checking and Verification

**Model Checking Process Scheduling over Multi-core Computer System with MSVL** ................................................................. 103  
*Xinfeng Shu and Zhenhua Duan*

**A Method Based on MSVL for Verification of the Social Network Privacy Policy** ................................................................. 118  
*Xiaobing Wang and Tao Sun*

**A Case Study: SOFL + Model Checking for OSEK/VDX Application** ........ 132  
*Zhuo Cheng, Haitao Zhang, Yasuo Tan, and Yuto Lim*
On Reachability Analysis of Updatable Timed Automata with One Updatable Clock .......................... 147  
  *Yunqing Wen, Guoqiang Li, and Shoji Yuen*

**Algorithm and Transformation**

Research on Formal Development of Non-recursive Algorithms of Graph Search ................................ 165  
  *Qimin Hu, Jinyun Xue, and Zhen You*

LtlNfBa: Making LTL Translation More Practical ................................. 179  
  *Cong Tian, Jun Song, Zhenhua Duan, and Zhao Duan*

PPTL_SPIN: A SPIN Based Model Checker for Propositional Projection Temporal Logic ........................................ 195  
  *Xiaoming Zhang, Zhenhua Duan, and Cong Tian*

Automatic Transformation from SOFL Module Specifications to Program Structures .......................... 206  
  *Xiongwen Luo and Shaoying Liu*

**Author Index** ................................................................. 219